Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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HAT2024R

Silicon N Channel Power MOS FET High Speed Power Switching

REJ03G1159-0500

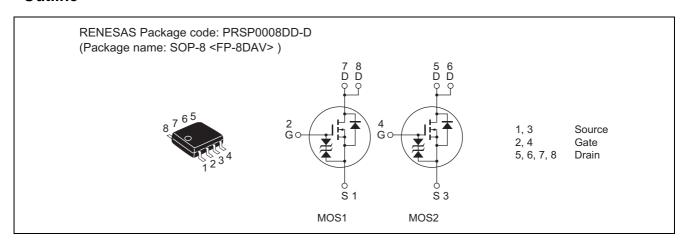
(Previous: ADE-208-494C)

Rev.5.00 Sep 07, 2005

Features

- Low on-resistance
- Capable of 4 V gate drive
- Low drive current
- High density mounting

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit
Drain to source voltage	V _{DSS}	30	V
Gate to source voltage	V _{GSS}	±20	V
Drain current	I _D	5.5	Α
Drain peak current	I _{D (pulse)} Note 1	44	Α
Body-drain diode reverse drain current	I _{DR}	5.5	А
Channel dissipation	Pch Note 2	2	W
Channel dissipation	Pch Note 3	3	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

- 2. 1 Drive operation: When using the glass epoxy board (FR4 $40 \times 40 \times 1.6$ mm), PW ≤ 10 s
- 3. 2 Drive operation: When using the glass epoxy board (FR4 $40 \times 40 \times 1.6$ mm), PW ≤ 10 s

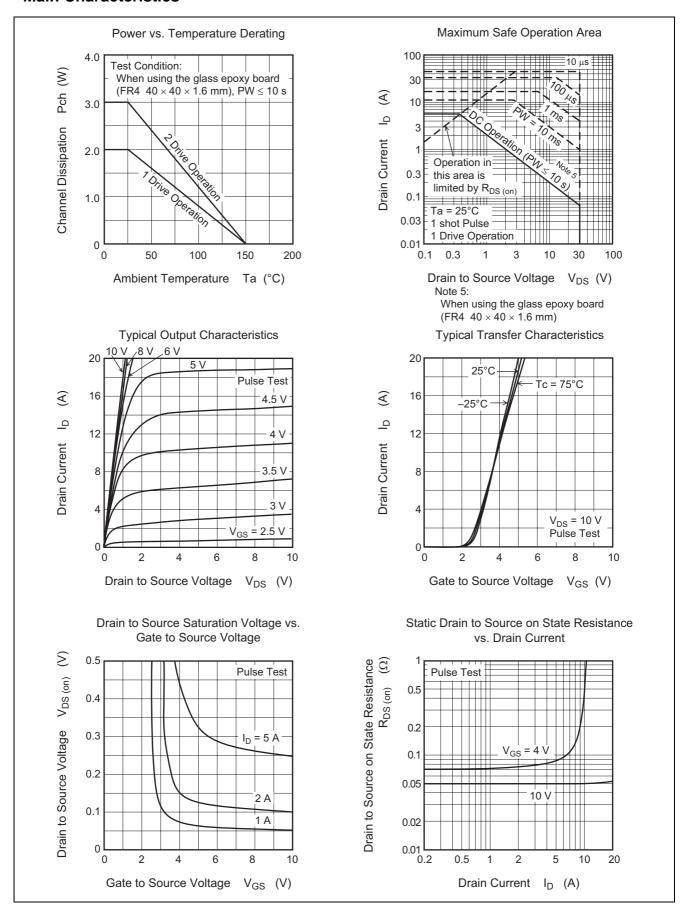
Electrical Characteristics

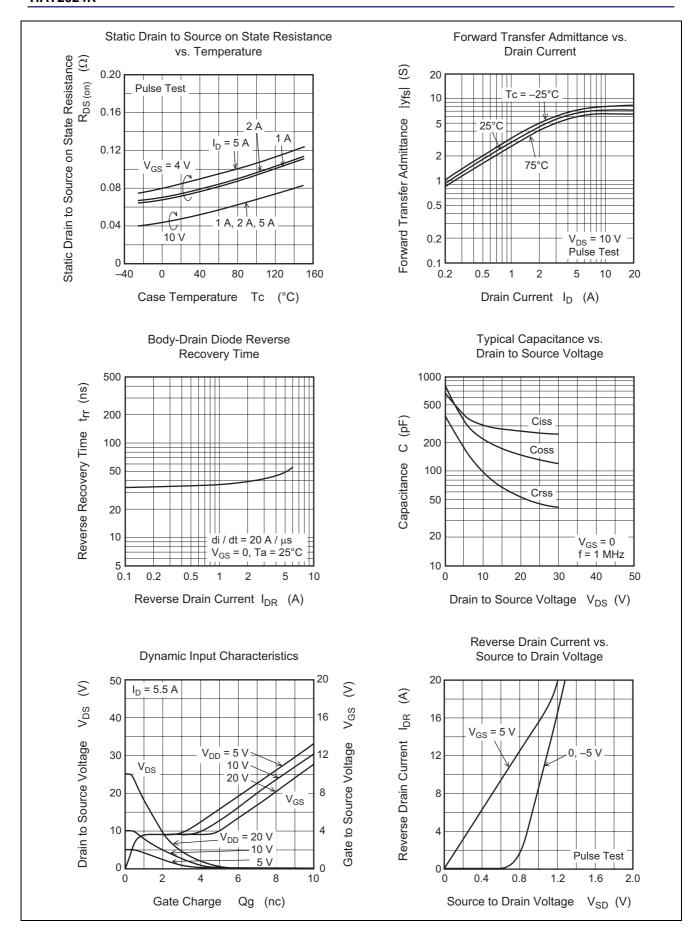
 $(Ta = 25^{\circ}C)$

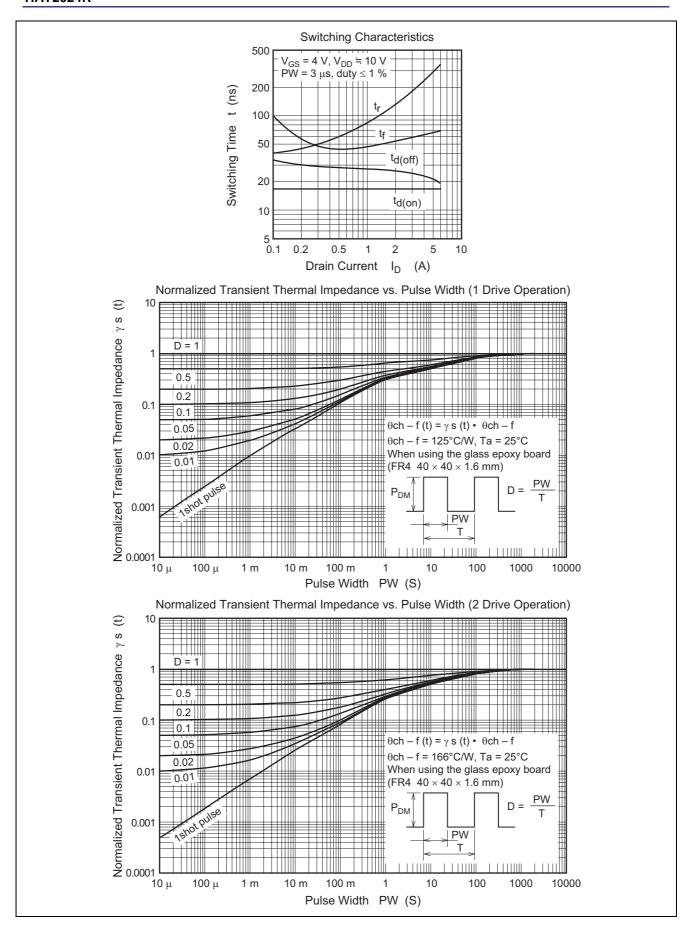
Item	Symbol	Min	Тур	Max	Unit	Test Conditions	
Drain to source breakdown voltage	V _{(BR) DSS}	30	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$	
Gate to source breakdown voltage	V _{(BR) GSS}	±20	_	_	V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$	
Gate to source leak current	I _{GSS}	_	_	±10	μΑ	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$	
Zero gate voltage drain current	I _{DSS}	_	_	10	μΑ	$V_{DS} = 30 \text{ V}, V_{GS} = 0$	
Gate to source cutoff voltage	V _{GS (off)}	1.0	_	2.0	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$	
Static drain to source on state resistance	R _{DS (on)}	_	0.05	0.065	Ω	$I_D = 3 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note 4}}$	
	R _{DS (on)}	_	0.078	0.11	Ω	$I_D = 3 \text{ A}, V_{GS} = 4 \text{ V}^{\text{Note 4}}$	
Forward transfer admittance	y _{fs}	3.5	5.5	_	S	$I_D = 3 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note 4}}$	
Input capacitance	Ciss	_	310	_	pF	V _{DS} = 10 V	
Output capacitance	Coss	_	220	_	pF	$V_{GS} = 0$	
Reverse transfer capacitance	Crss	_	100	_	pF	f = 1 MHz	
Turn-on delay time	t _{d (on)}	_	17	_	ns	$V_{GS} = 4 \text{ V}, I_D = 3 \text{ A},$	
Rise time	t _r	_	190	_	ns	V _{DD} ≅ 10 V	
Turn-off delay time	t _{d (off)}	_	25	_	ns		
Fall time	t _f	_	60	_	ns		
Body-drain diode forward voltage	V_{DF}	_	0.9	1.4	V	$I_F = 5.5 \text{ A}, V_{GS} = 0^{\text{Note 4}}$	
Body-drain diode reverse recovery time	t _{rr}	_	50	_	ns	I _F = 5.5 A, V _{GS} = 0	
						$di_F/dt = 20 A/\mu s$	

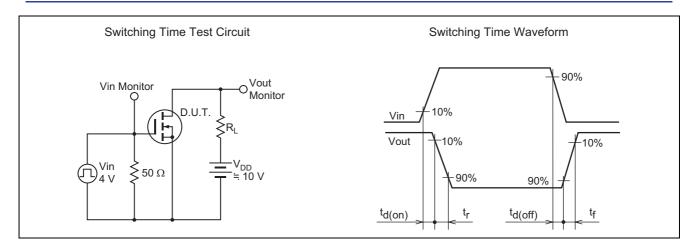
Note: 4. Pulse test

Main Characteristics

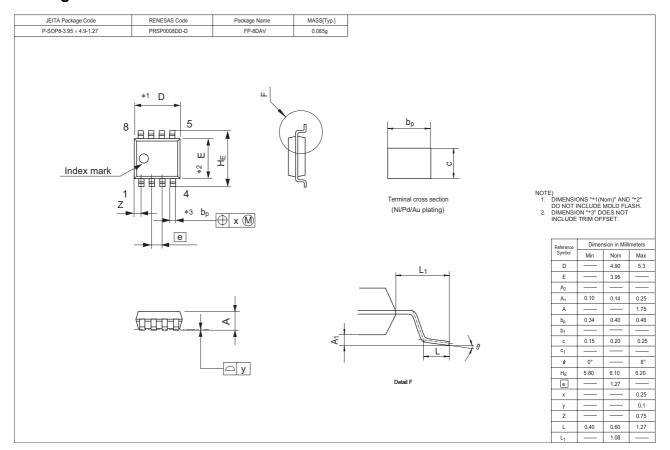








Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container
HAT2024R-EL-E	2500 pcs	Taping

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